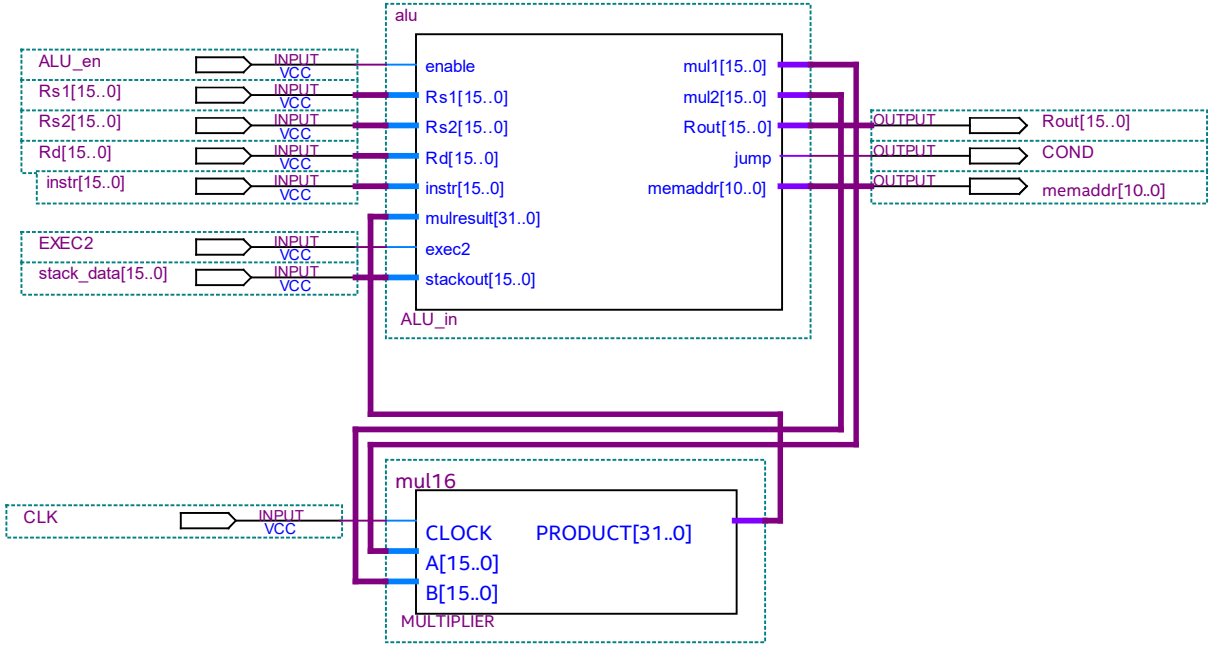
alu
ALU_en
INPUT
VCC
Rs1[15..0]
Rs2[15..0]
Rd[15..0]
instr[15..0]
EXEC2
stack_data[15..0]
enable
Rs1[15..0]
Rs2[15..0]
Rd[15..0]
instr[15..0]
mulresult[31..0]
exec2
stackout[15..0]
mul1[15..0]
mul2[15..0]
Rout[15..0]
jump
memaddr[10..0]
ALU_in
OUTPUT
Rout[15..0]
COND
memaddr[10..0]
mul16
CLK
CLOCK
PRODUCT[31..0]
A[15..0]
B[15..0]
MULTIPLIER